# Monolithic Dual Switching Diode

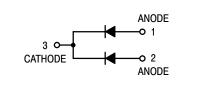
### Features

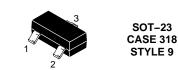
• Pb–Free Package is Available



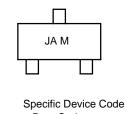
# **ON Semiconductor®**

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### MARKING DIAGRAM



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### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BAV74LT1	SOT-23	3000 / Tape & Reel
BAV74LT1G	SOT–23 (Pb–Free)	3000 / Tape & Reel
BAV74LT3	SOT-23	10,000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### MAXIMUM RATINGS (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	V <sub>R</sub>	50	Vdc
Forward Current	١ <sub>F</sub>	200	mAdc
Peak Forward Surge Current	I <sub>FM(surge)</sub>	500	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1), $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	P <sub>D</sub>	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

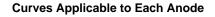
1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.

2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in 99.5% alumina.

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Breakdown Voltage (I <sub>(BR)</sub> = 5.0 μAdc)	V <sub>(BR)</sub>	50	-	Vdc
Reverse Voltage Leakage Current, (Note 3) ( $V_R = 50 \text{ Vdc}, T_J = 125^{\circ}\text{C}$ ) ( $V_R = 50 \text{ Vdc}$ )	I <sub>R</sub>		100 0.1	μAdc
Diode Capacitance ( $V_R = 0, f = 1.0 \text{ MHz}$ )	CD	-	2.0	pF
Forward Voltage (I <sub>F</sub> = 100 mAdc)	V <sub>F</sub>	-	1.0	Vdc
Reverse Recovery Time (I <sub>F</sub> = I <sub>R</sub> = 10 mAdc, I <sub>R(REC)</sub> = 1.0 mAdc, measured at I <sub>R</sub> = 1.0 mA, R <sub>L</sub> = 100 $\Omega$ )	t <sub>rr</sub>	-	4.0	ns

3. For each individual diode while the second diode is unbiased.



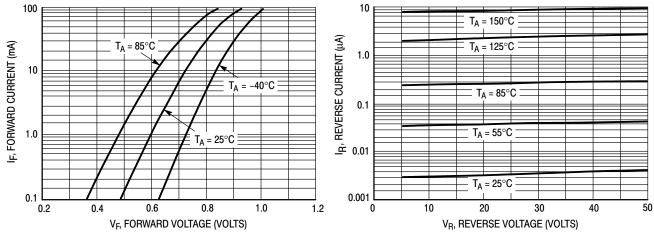
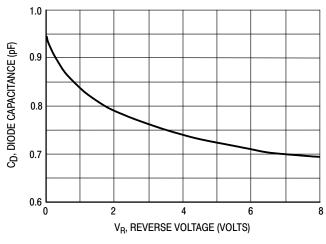




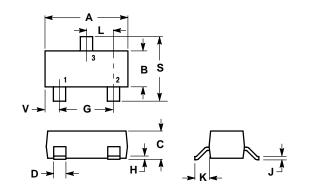
Figure 2. Leakage Current





### PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AH** 

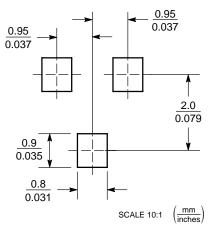


- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. 4. 318–03 AND -07 OBSOLETE, NEW STANDARD 318–08.
- STANDARD 318-08.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.1102	0.1197	2.80	3.04
В	0.0472	0.0551	1.20	1.40
С	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
Н	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
κ	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
v	0.0177	0.0236	0.45	0.60

STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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